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SkGupta is a leading biochemistry publishing company with a 12-year history. Over 30,000 downloads for this product! crack windows 7 pro free download word1. Field of the Invention The present invention relates to a method of fabricating a semiconductor device. More particularly, the present invention relates to a semiconductor device having a layer of semiconductor material capable of altering the electrical characteristics of the semiconductor device, including a resistor. The present invention also relates to a method of fabricating such a semiconductor device. 2. Description of Related Art As is well known, conductive lines, including gate lines or interconnect lines, are often formed in semiconductor devices. This is typically accomplished by etching trenches or vias (holes) into a layer of dielectric material deposited over a semiconductor substrate, and then filling the etched vias and trenches with a conductive material, such as polysilicon, tungsten, or other conductive metal or metal compound. The conductive material in the vias and trenches provides a means for interconnecting various devices in an integrated circuit. Various devices, such as diodes and transistors, are fabricated on a semiconductor substrate to form an integrated circuit. The conductive lines, such as the gate lines, typically act as part of a switching circuit for the various devices in the integrated circuit. The conductive lines may be formed by ion implantation, physical vapor deposition, or by various other known deposition techniques. There are many instances in the fabrication of semiconductor devices when the formation of a layer of conductive material, such as polysilicon, tungsten, or other conductive metal or metal compound, is required. Exemplary situations in which the formation of such a layer of conductive material may be necessary include, but are not limited to, back end of

line (BEOL) processing, formation of gate electrodes, formation of source/drain regions, and formation of conductive lines for connecting devices or making electrical contacts. When forming a gate electrode from a conductive material, such as polysilicon or tungsten, the conductive material is typically formed over the entire substrate surface and, then, is selectively removed from the appropriate areas to form the desired gate electrode. As is well known, there are many problems that